

Synopsys University Bundle -FrontEnd

Bundle	Product#	Description	Function
Synopsys 3900-0 Asia Pac FrontEnd University Bundle	2618-0	coreBuilder	coreBuilder
	2673-0	Library Compiler Base	Library pack
	2925-0	DesignWare Library	Library pack
	2926-0	DesignWare Developer	Library pack
	3353-0	CoreAssembler	CoreAssembler
	4643-0	WaveView Elite	Analog wave viewer and Analysis/reporting
	4874-0	Synplify Apex	Logic Synthesis for FPGA
	C040-0	NanoTime Base	Transistor-level Static Signoff Analysis
	C070-0	Hybrid Add-on	Hybrid Add-on
	C877-0	VC Formal FSV Add-on	Formal Verification, debug
	D861-0	AI Exploration Pack	AI Exploration Pack
	D982-0	VC Formal FuSa Add-on	Formal Verification, debug
	E059-0	TestMAX Manager Add-On	Common Interface for DFT capabilities
	E060-0	TestMAX XLBIST Add-On	Fully X-tolerant, high coverage logic BIST
	E323-0	VCS Legacy Add-on	Verilog/VHDL Simulator (X-Prop, UPF-Low Power)
	E384-0	TestMAX Access Pkg	Design For Test (DFT)
	E910-0	Formality FuSa Add-on	Equivalence Checking (EC)
	F819-0	TestMAX ALE Base	Design For Test (DFT)
	F974-0	Verdi Portable Stimulus Add-On	Design Debug & Analysis (Coverage, etc)
	F975-0	VC Portable Stimulus RT Synopsys	Stimulus Generation and coverage closure across multiple verification engines
	G402-0	TestMAX Advisor Base	TestMAX Advisor Base
	G404-0	TestMAX ATPG Elite	Defects defective parts in minimal test time
	G405-0	TestMAX DFT Apex	Design For Test (DFT)
	G408-0	TestMAX Diagnosis Base	Design For Test (DFT)
G409-0	TestMAX SMS Base	Design For Test (DFT)	
G410-0	TestMAX Vtran Base	Design For Test (DFT)	

G415-0	PrimePower Elite	power analysis
G419-0	PrimeTime Apex	Pre/Post Static Timing Analysis
G430-0	IC Compiler II Elite	IC Compiler II Elite
G432-0	Formality Elite	Equivalence Checking (EC)
G435-0	PrimePower RTL Elite Pkg	power analysis
G437-0	Fusion Compiler Elite Pkg	RTL-to-GDSII implementation
G440-0	Design Compiler Elite Pkg	RTL Synthesis for ASIC
G442-0	RTL Architect Elite Pkg	RTL analysis (predict the power, performance, area)
G831-0	Euclide Elite	Finds Bugs Early & Accelerates Design and Verification Productivity
G834-0	VCS Apex	Verilog/VHDL Simulator (X-Prop, UPF-Low Power)
G838-0	Verdi Apex	Design Debug & Analysis (Coverage, etc)
G841-0	VC Formal Elite	Formal Verification, debug
G844-0	VC LP Elite	Low Power Design and Verification
G847-0	VC SpyGlass Apex	RTL Design Static Anlysis (Lint, CDS, DFT, Power)
G849-0	VC VIP SOC Library Base 10 Pack	Liabrary pack
G851-0	Platform Architect Elite	early analysis and optimization of multicore SoC architectures
G871-0	VC SpyGlass Fault Analysis Elite	RTL Design Static Anlysis (Lint, CDS, DFT, Power)
H161-0	Verdi UPF Architect Add-on	Design Debug & Analysis (Coverage, etc)
H225-0	PrimeLib Base	library characterization
H226-0	PrimeLib Simulator Add on	library characterization
H505-0	VC Formal DPV Elite	Formal Verification, debug
H577-0	Formality ECO Elite	Equivalence Checking (EC)
H655-0	Timing Constraints Manager Apex	Fault Simulation (injects faults)
H694-0	IC Validator Apex	IC Validator Apex
H768-0	VC Z01X Fault Simulator Elite	Fault Simulation (injects faults)

Synopsys University Bundle -BackEnd

Bundle	Product#	Description	Function
Synopsys 3901-0 Asia Pac BackEnd University Bundle	2673-0	Library Compiler Base	Liabrary pack
	4311-0	PrimeSim CCK Base	FastSPICE Simulator
	4643-0	WaveView Elite	Analog wave viewer and Analysis/reporting
	4660-0	PrimeSim HSPICE Base	Analog circuit simulator
	A043-0	Laker Placement Add-On	Custom Design Tools(Layout)
	A044-0	Laker Routing Add-On	Custom Design Tools(Layout)
	A050-0	Laker FPD Elite	Custom Design Tools(Layout)
	A052-0	Laker Blitz Base	Custom Design Tools(Layout)
	A088-0	SiliconSmart eFineSim Pro Memry	Library Cell (Standard,IO) Characterization
	C040-0	NanoTime Base	Transistor-level Static Signoff Analysis
	E005-0	Laker Apex	Custom Design Tools
	E260-0	Custom Compiler Apex	Custom design solution. Includes schematic & layout
	E323-0	VCS Legacy Add-on	Verilog/VHDL Simulator (X-Prop, UPF-Low Power)
	F511-0	IC Validator Workbench	Physical Verification - DRC or LVS checking
	F949-0	PrimeSim Continuum Tokens	FastSPICE Simulator
	F956-0	VCS PrimeSim AMS Add-on	Mixed-signal verification solution incorporating VCS functional verification and CustomSim FastSPICE co-simulation
	G415-0	PrimePower Elite	power analysis
	G419-0	PrimeTime Apex	Pre/Post Static Timing Analysis
	G425-0	StarRC Apex	Physical Layout Parasitic Extraction
	G426-0	StarRC Digital Add-On	Physical Layout Parasitic Extraction
G427-0	StarRC Custom Add-On	Physical Layout Parasitic Extraction	
G430-0	IC Compiler II Elite	Auto Place and Route	

	G432-0	Formality Elite	Equivalence Checking (EC)
	G437-0	Fusion Compiler Elite Pkg	RTL-to-GDSII implementation
	G831-0	Euclide Elite	Finds Bugs Early & Accelerates Design and Verification Productivity
	G834-0	VCS Apex	Verilog/VHDL Simulator (X-Prop, UPF-Low Power)
	G838-0	Verdi Apex	Design Debug & Analysis (Coverage, etc)
	H161-0	Verdi UPF Architect Add-on	Design Debug & Analysis (Coverage, etc)
	H203-0	ESP Elite	Formal Equivalence Checking Based on Symbolic Simulation
	H225-0	PrimeLib Base	library characterization
	H226-0	PrimeLib Simulator Add on	library characterization
	H457-0	PrimeClosure Apex	PrimeClosure Apex
	H694-0	IC Validator Apex	Physical Verification - DRC or LVS checking

Synopsys University Bundle -TCAD

Bundle	Product#	Description	Function
Synopsys 4458-0 AsiaPac Advanced TCAD Univ. Bundle	4347-0	TCAD Sentaurus Workbench	Simulation environment
	4348-0	TCAD Sentaurus Workbench Advanced	Automatic generation of design of experiment splits, analysis and optimization
	4350-0	TCAD Sentaurus Process	Advanced 1D and 2D silicon process simulator
	4351-0	TCAD Sentaurus Process 3D	Enables 3D simulation of silicon processing
	4352-0	TCAD Sentaurus Device	Comprehensive 1D and 2D device simulator based on the drift- diffusion equations
	4353-0	TCAD Sentaurus Device Advanced	Add-on to TCAD Sentaurus Device (4352-0) with advanced features
	4354-0	TCAD Sentaurus Device DSM	Gate leakage models, floating gates, ferroelectric materials, radiation models, and simulation of oxide degradation (reliability)
	4355-0	TCAD Sentaurus Device Compound	heterostructure device simulation
	4357-0	TCAD Sentaurus Device 3D	3D device simulation and 3D mixed- element mesh generation
	4358-0	TCAD Sentaurus Parallel	Parallel processing: 4 Thread Shared Memory Pack
	4359-0	TCAD Sentaurus Device EMW	Add-on to TCAD Sentaurus Device(4352-0) with simulation module
	4360-0	TCAD Sentaurus Device Monte Carlo	Full-band Monte Carlo simulator for silicon
	4361-0	TCAD Sentaurus Device Power	Add-on to TCAD Sentaurus Device (4352-0) with support for semiconductor materials other than silicon
	4362-0	TCAD Sentaurus PCM Studio	Suite of tools to explore and analyze process-device relationships statistically
	4363-0	TCAD Sentaurus PCM Library	Suite of tools to explore and analyze process-device relationships statistically

	4375-0	TCAD Sentaurus Structure Editor	2D structure editor based on solid modeling engine
	4376-0	TCAD Sentaurus Structure Editor 3D	Enables 3D structure generation and editing
	4476-0	TCAD Sentaurus Process Kinetic MC	Unique process simulator that uses an atomistic approach to dopant, point-defect and extended-defect interactions
	7620-0	TCAD Sentaurus Device EMW MPI	FDTD distributed compute using message-passing interface (MPI) or GPUs
	7998-0	TCAD Sentaurus Interconnect	Advanced 3D simulation of interconnect stress and chip-packaging interaction (CPI) reliability
	9935-0	TCAD Sentaurus Visual	State-of-the-art interactive 1D, 2D, and 3D visualization of TCAD structures
	A708-0	Sentaurus Process Explorer	Process Emulator for exploration and integration of process technologies in 3D
	C023-0	TCAD Sentaurus Device QTX	Sub-band Boltzmann Equation and NEGF transport
	D917-0	Raphael FX	2D / 3D RC extraction; support for netlist extraction
	F511-0	IC Validator Workbench	Layout viewer/editor
	F512-0	Sentaurus Device KMC	Physical Verification - DRC or LVS checking
	G129-0	Silicon WorkBench	Layout viewer/editor

Overall Synopsys tool supported platforms are found here

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