

**** Cadence University Package ****

틀 기능 : Circuit/ Layout/ verification simulation(Full&Mixed&Semi_custom)

No.	Description	비고
1	Virtuoso(R) Schematic Editor HSPICE Interface	
2	Virtuoso(R) Schematic Editor Verilog(R) Interface	
3	Virtuoso(R) Analog HSPICE Interface Option	
4	Virtuoso(R) Schematic Editor L	
5	Virtuoso(R) Analog Design Environment XL	
6	Virtuoso(R) EDIF 200 Reader	
7	Virtuoso(R) EDIF 200 Writer	
8	Virtuoso(R) Layout Suite L	
9	Spectre(R) Classic Simulator	
10	Spectre(R) – RF option	
11	Incisive Enterprise Simulator – L	별도수요조사 후 배포
12	Genus Synthesis Solution (구, Encounter RTL Compiler-L)	별도수요조사 후 배포
13	Dracula Physical Verification and Extraction Suite	별도수요조사 후 배포
14	Innovus Implementation System	별도수요조사 후 배포
15	Cadence Quantus QRC Extraction – XL	별도수요조사 후 배포
17	Spectre Multi-mode Simulation(AMS 포함)	별도수요조사 후 배포
18	Assura(TM) Design Rule Checker	별도수요조사 후 배포
19	Assura(TM) Layout Vs. Schematic Verifier	별도수요조사 후 배포
20	Physical Verification System Design Rule Checker XL	별도수요조사 후 배포
21	Physical Verification System Layout vs. Schematic Checke	별도수요조사 후 배포